

Parallel Network-RAM Project

Semester Report – December 11th, 2003

John Oleszkiewicz

Summary of Work

- Implemented proposed network RAM System
 - New model is most realistic system yet
- Implemented vastly improved page fault generation algorithm
 - Faster
 - More realistic than previous models
- Speedups - simulator is now usable
- Debugging - many bugs found and eliminated
- Improved ease-of-use
 - Better configuration file setup
 - Configurable intermediate results demonstrate progress
- Technical Report is better written and up-to-date

To-Do List for February Paper Submission

- Fix communication model?
 - Portions are unrealistic
 - May take a significant amount of implementation and debugging time
- Continue debugging, if necessary
- Find support for paging model in literature
- Design and run experiments
- Write the paper

Notes

- I will be back from break on January 6th.
- January 6th - January 19th will be devoted to the project
- I **will** have a paper ready for submittal by the February deadline.
 - The only real question will be quality of the paper
 - The better research and development goes, the higher the quality...